

<b>Notice of Allowability</b>	Application No.	Applicant(s)
	09/527,281	DAVIES, ROBERT BRUCE
	Examiner	Art Unit
	David A. Zarneke	2827

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1.  This communication is responsive to the response filed 10/8/03 and the amendment filed 4/24/03.
2.  The allowed claim(s) is/are 1-11 and 17-24.
3.  The drawings filed on 3/17/00 are accepted by the Examiner.
4.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a)  All
  - b)  Some\*
  - c)  None
 of the:
  1.  Certified copies of the priority documents have been received.
  2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3.  Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

5.  A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6.  CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.
  - (a)  including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached
    - 1)  hereto or 2)  to Paper No./Mail Date \_\_\_\_\_.
  - (b)  including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7.  DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

#### Attachment(s)

1.  Notice of References Cited (PTO-892)
2.  Notice of Draftsperson's Patent Drawing Review ( PTO-948)
3.  Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_
4.  Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5.  Notice of Informal Patent Application (PTO-152)
6.  Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
7.  Examiner's Amendment/Comment
8.  Examiner's Statement of Reasons for Allowance
9.  Other \_\_\_\_\_.

## **NOTICE OF ALLOWANCE**

### ***Drawings***

All objections to the drawings have been removed. The drawings are acceptable as they are currently presented.

### ***Examiner's Amendment***

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows: In claim 3, line 8, prior to "base", replace "the" with --a--.

### ***Reasons for Allowance***

The following is an examiner's statement of reasons for allowance:

Regarding claims 1 and 3, prior art could not be found that taught a first semiconductor die having a first surface and an opposed second surface, the first surface being formed with a first cavity mounting the first semiconductor die on the first pedestal and second smaller cavity within the first cavity underlying a reactive component mounting region defined on the second surface. This limitation, in

conjunction with the other claim limitations, was neither disclosed in, nor suggested by, the prior art.

With respect to claim 17, prior art could not be found that taught a semiconductor die having a first surface and an opposed second surface, the first surface being formed with a recession, and the second surface being formed with a dielectric platform having a lower surface in the recession and an upper surface defining a reactive component mounting region coplanar with the second surface, and a reactive component formed on the second surface of the semiconductor die in the reactive component mounting region to overlie the recessed region of the raised appurtenance. This limitation, in conjunction with the other claim limitations, was neither disclosed in, nor suggested by, the prior art.

As to claim 18, prior art could not be found that taught a die attach mount comprising a die attach pedestal formed on the die mounting surface and configured to engage with a first semiconductor die having a cooperating recess formed in a rear surface where the die attach pedestal has a recessed region in a surface thereof for forming dielectric volume in cooperation with the rear surface of the first semiconductor die. This limitation, in conjunction with the other claim limitations, was neither disclosed in, nor suggested by, the prior art.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

***Conclusion***

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The references cited on the attached PTO-892 all show the state of the art.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David A. Zarneke whose telephone number is (571)-272-1937. The examiner can normally be reached on M-F 10 AM-6PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamand Cuneo can be reached on (571)-272-1957. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



David A. Zarneke  
Primary Examiner  
March 2, 2004